



JSPM UNIVERSITY PUNE
Faculty of Science and Technology
School of Electrical and Communication Sciences
Course Structure: M. Tech (VLSI and Embedded Systems)

Semester – I
Level - 6.5

Sr. No.	Course Type	Course Code	Course Name	Teaching Scheme (Hrs. per week)				Examination & Marking Scheme			Credits
				L	T	P	EL	TH	PR	OR	
01	PSMC	230GMAM02_01	Advanced Numerical Methods	2	1	-	-	100	-	-	3
02	PCC	230GETM01_01	Analog CMOS VLSI Design	2	-	-	-	100	-	-	2
03	PCC	230GETM02_01	Advanced Embedded System	2	-	-	2	100	-	-	2.5
04	PCC	230GETM03_01	Low Power VLSI Circuit Design	3	-	-	-	100	-	-	3
05	MMC	230GRAM24_01	Sensors and Automation	1	-	2	-	-	50	50	2
06	SEC	230GCSM19_01	R-Programming	2	-	2	-	-	50	50	3
07	VSC	230IDCB01_01	Design Thinking and Creativity	1	-	-	2	-	-	50	1.5
08	AEC	230UENM01_01	Communicative English for Professionals	1	-	2	-	50	-	-	2
09	RMC	230IRMM01_01	Research Methodology	2	-	-	-	50	-	-	2
10	LC	230GETM21_01	Advanced Embedded System Lab	-	-	2	-	-	50	-	1
Total Academic Engagement and Credits				16	1	8	4	500	150	150	22
				25				800			

Dr. Santoshkumar Hampannavar
 Director-SECS
 FST

Dr. R.S. Deshpande
 Dean
 FST

Dr. B.B. Ahuja
 Vice Chancellor
 JSPM University
 Pune



JSPM UNIVERSITY PUNE
Faculty of Science and Technology
School of Electrical and Communication Sciences
Course Structure: M. Tech (VLSI and Embedded Systems)

Semester – II
Level – 6.5

Sr. No.	Course Type	Course Code	Course Name	Teaching Scheme (Hrs. per week)				Examination & Marking Scheme			Credits
				L	T	P	EL	TH	PR	OR	
01	PSBC	230GETM20_02	Basics of IC Fabrication	2	1	-	-	100	-	-	3
02	PCC	230GETM04_02	RTL Synthesis and Simulation	2	-	-	2	100	-	-	2.5
03	PCC	230GETM05_02	Digital CMOS VLSI Design	2	-	-	-	100	-	-	2
04	MMC	230GISM03_02	Cyber Security	1	-	2	-	-	50	50	2
05	SEC	230GETM19_02	System Design with Embedded Linux	2	-	2	-	-	50	50	3
06	VSC	230IINB02_02	Innovation	1	-	-	2	-	-	50	1.5
07	AEC	230UENM02_02	Business Communication	1	-	2	-	50	-	-	2
08	RMC	230IRMM02_02	Research Design and Techniques	2	-	-	-	50	-	-	2
09	LC	230GETM22_02	RTL Synthesis and Simulation lab	-	-	2	-	-	50	-	1
10	LC	230GETM23_02	Digital CMOS VLSI Design Lab	-	-	2	-	-	50	-	1
11	IITP/F P/CEP	230GETM24_02	Internship/Field Project/Community Engagement Programme	4 to 6 weeks				-	-	50	2
Total Academic Engagement and Credits				13	1	10	4	400	200	200	22
				24				800			

Note: A Postgraduate Diploma will be awarded if a student exits after first year.

For Exit at the end of first year the student must complete: (Total credits = 8)

- a) An internship / OJT of 8 - 10 weeks (4 credits)
- b) Additional Course 1 (4 credits) (Vocational Skill Course (VSC) / Skill Enhancement Course (SEC))

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